



Design Your First AME Sample

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AME Design Rules

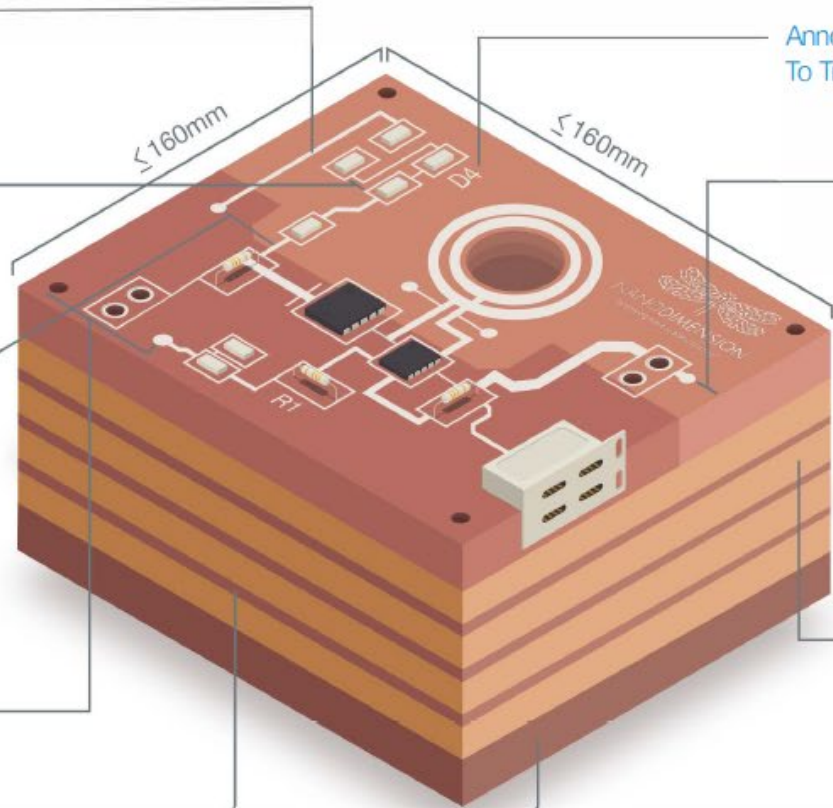
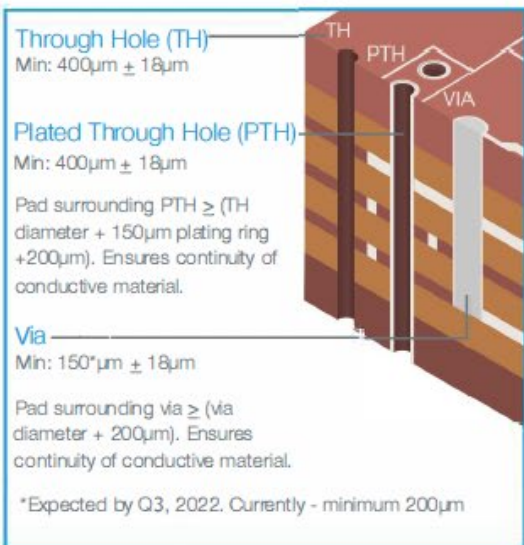
The design rules are not technical limitations of the system, but process parameters to ensure a high yield

Min Trace Width $75\mu\text{m} \pm 9\mu\text{m}$

Min Electrical Clearance / Space

Clearance/Space	Trace Thickness
$100\mu\text{m} \pm 9\mu\text{m}$	$17\mu\text{m} - 50\mu\text{m}$
$150\mu\text{m} \pm 9\mu\text{m}$	$50\mu\text{m} - 100\mu\text{m}$

Minimum PTH/Via release/clearance: $250\mu\text{m}$



Annotation Clearance
To Trace $> 180\mu\text{m}$

Edge Spacing
 0.5mm (When no edge plating)

$0.7\text{mm} \leq \text{Thickness} \leq 3\text{mm} \pm 5\%$

Minimum Prepreg Above Layer

Signal/plane thickness $\pm 5\%$	Min Prepreg above layer
$17 - 34\mu\text{m}$	$50\mu\text{m}$
$35 - 69\mu\text{m}$	$75\mu\text{m}$
$70 - 99\mu\text{m}$	$125\mu\text{m}$
$100\mu\text{m}$	$150\mu\text{m}$

Signal / Plane
Layer Thickness
Min: $17\mu\text{m}$, $6\mu\text{m}$
steps
up to $100\mu\text{m}$

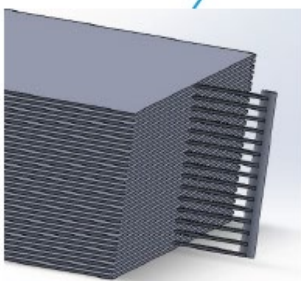
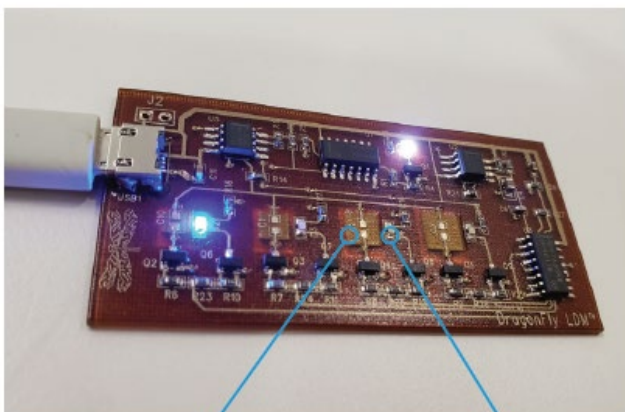
Top / Bottom Solder
Mask Thickness
 $50\mu\text{m}$

Get Your First AME Board Printed

Applicability terms:

- Design comply with AME Design rules
- Up to 50mm X,Y size
- Up to 1.6mm height

3D Printed Electronics Opens New Design Possibilities

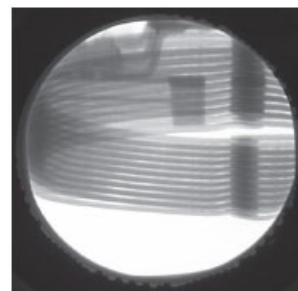
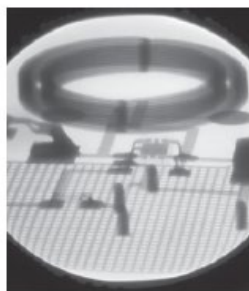


Design

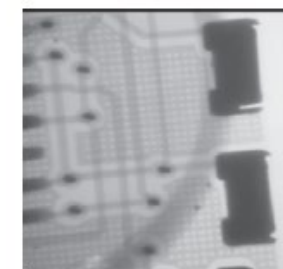
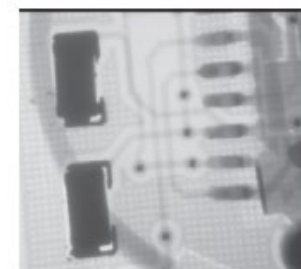
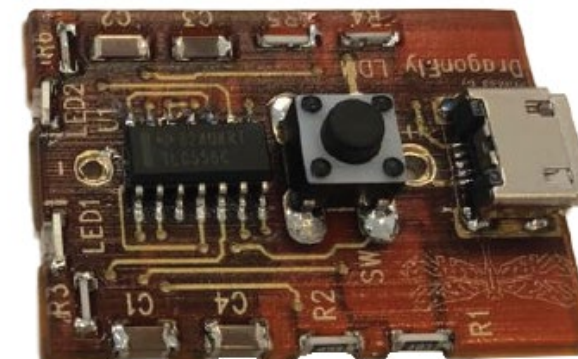


Actual

Printed Capacitors



Printed coil (DC-DC)



Side Mounting

Submit your Design and get it printed

- Send you design in one of the following formats:
 - ODB ++
 - Gerbers & Excellon with Route and Stackup info
- Submit your design or ask a question on the link below:
<https://www.nano-di.com/think-3d-design-submission>



THANK YOU



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www.nano-di.com